

FIG. 1A

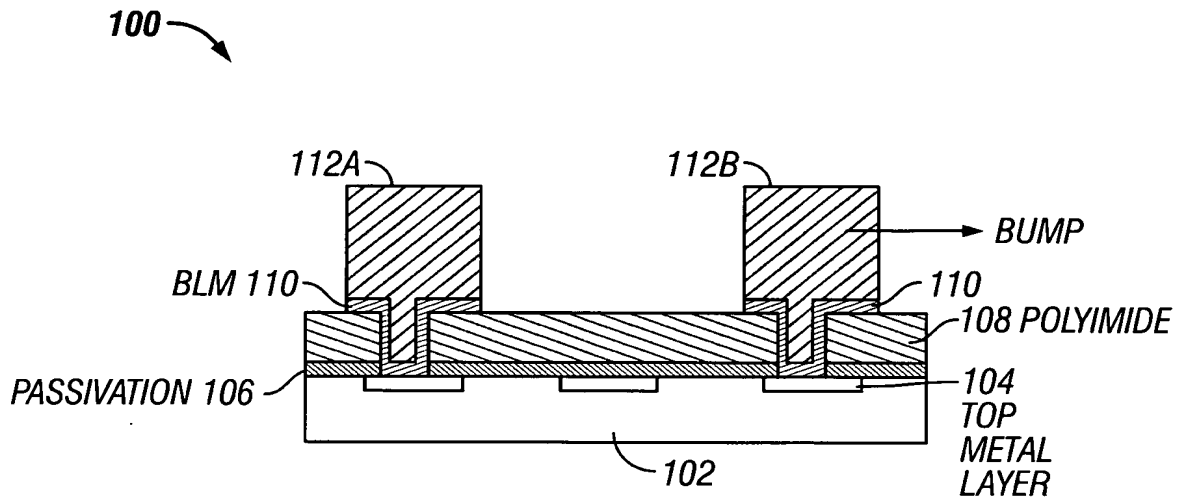


FIG. 1B

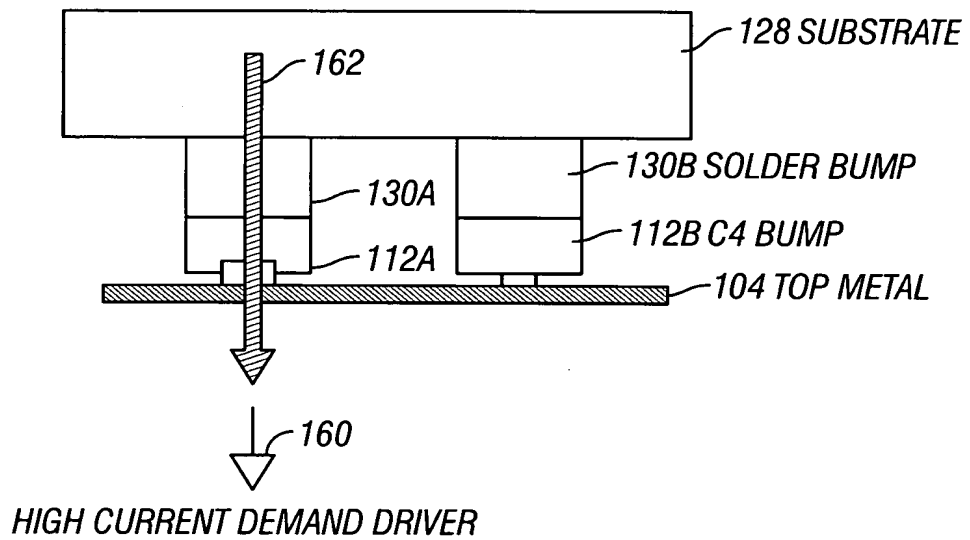


FIG. 1C

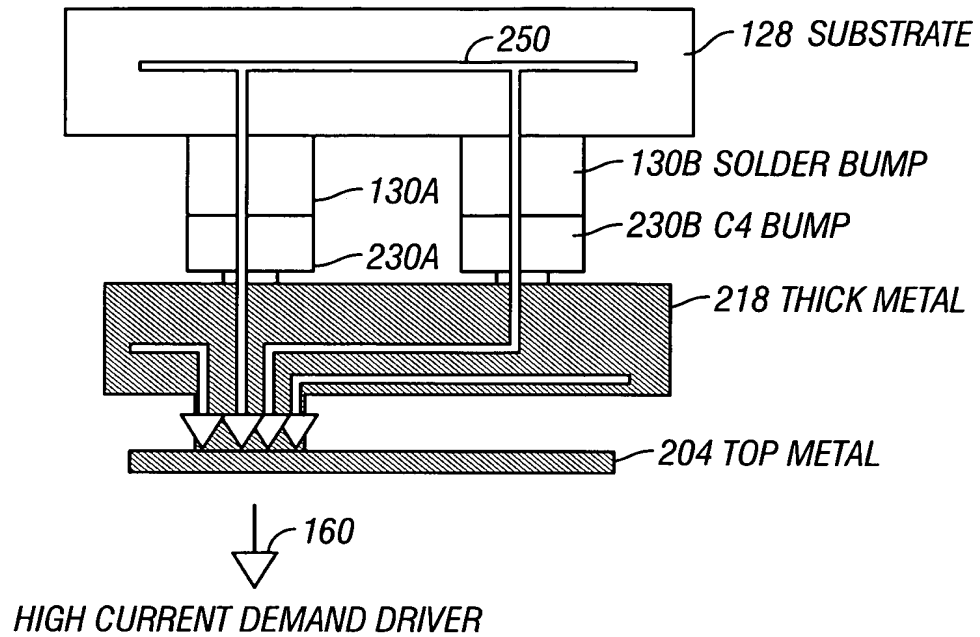


FIG. 1D

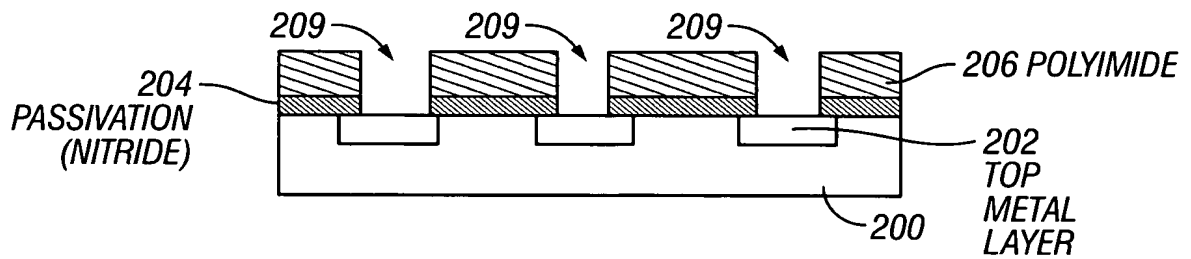


FIG. 2

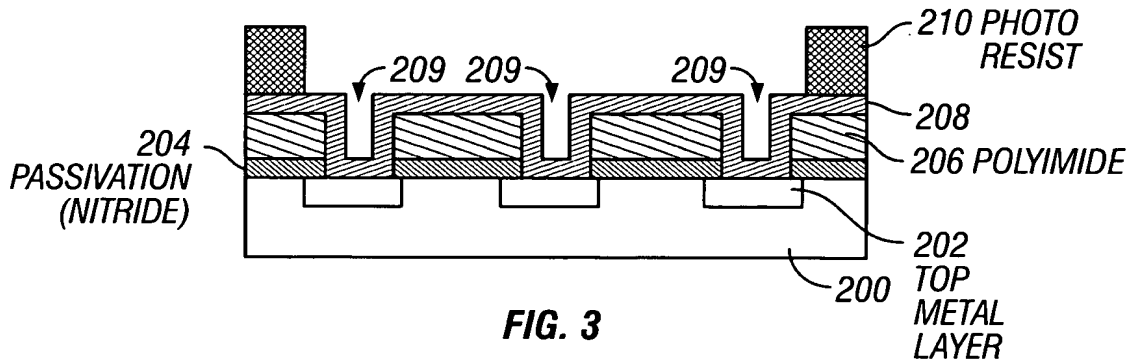


FIG. 3

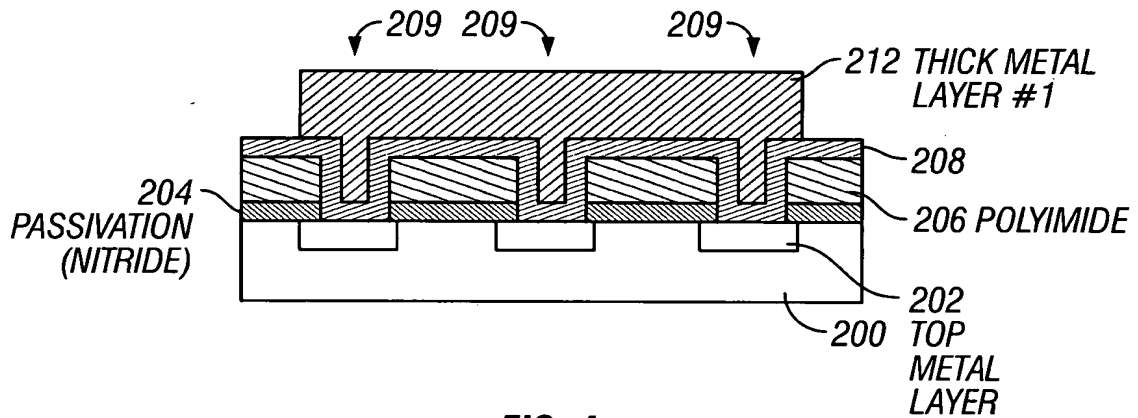


FIG. 4

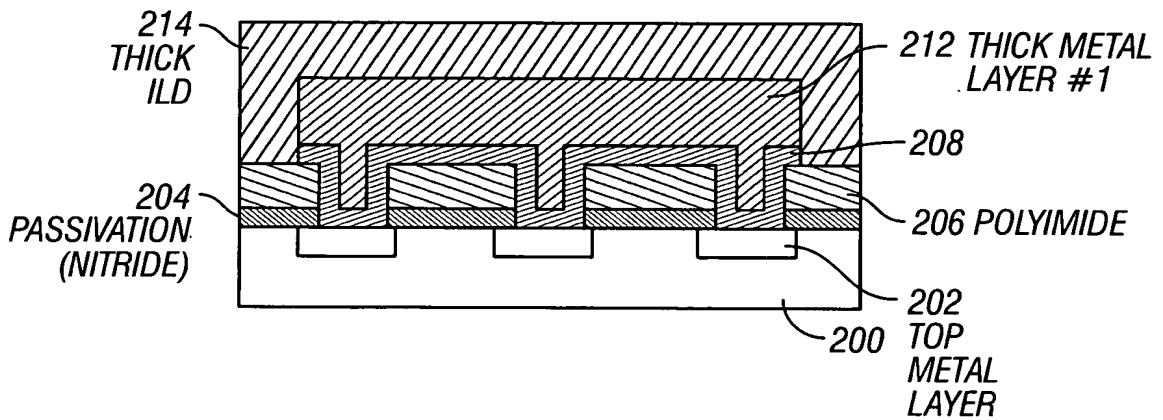


FIG. 5

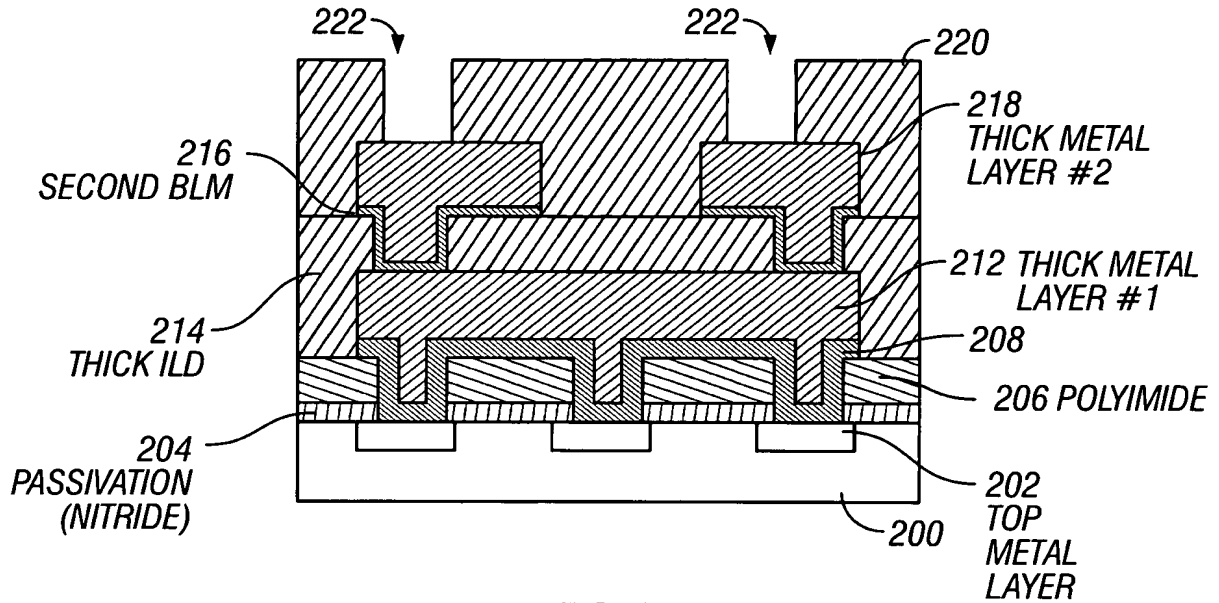


FIG. 6

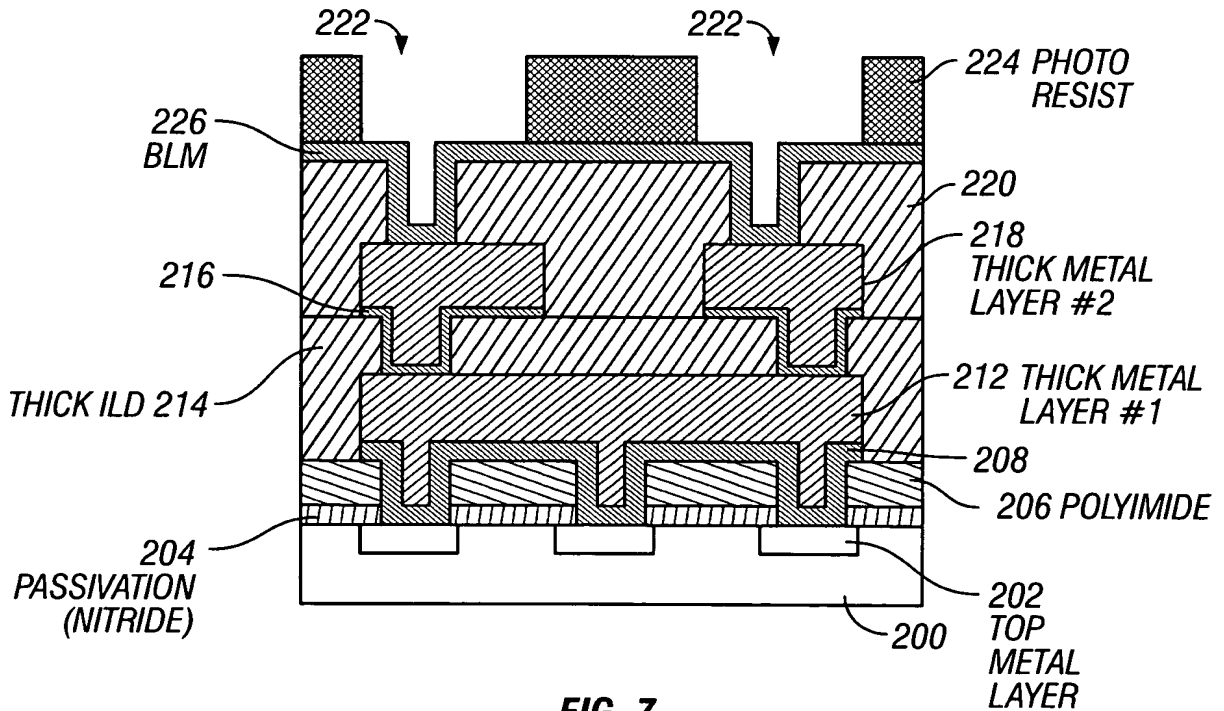


FIG. 7

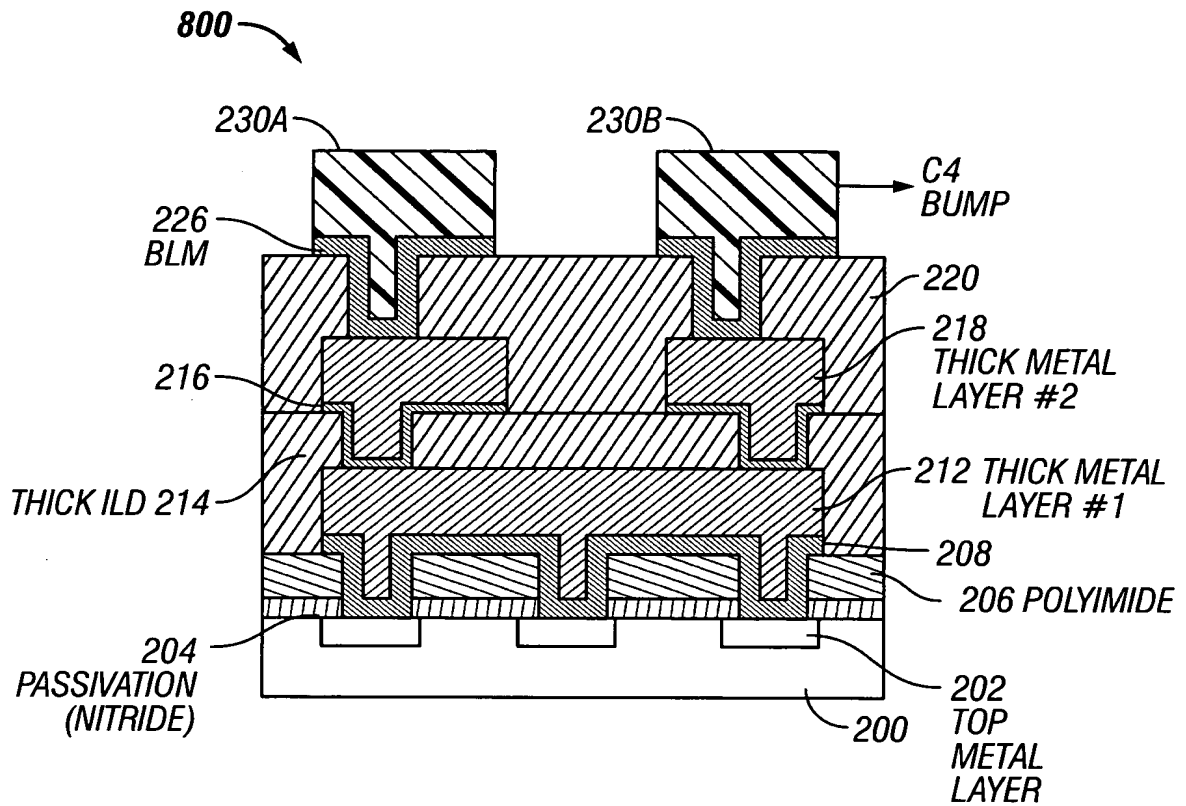


FIG. 8A

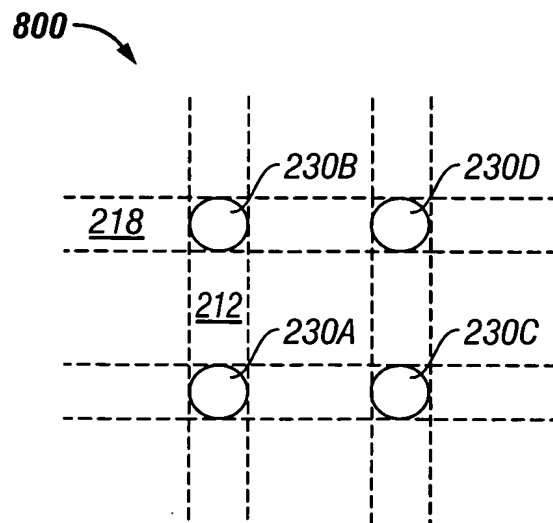


FIG. 8B

Applicant(s): Sarah E. Kim et al.

THICK METAL LAYER INTEGRATED PROCESS FLOW TO
IMPROVE POWER DELIVERY AND MECHANICAL
BUFFERING

FLOW 1	
	1. NO CU DIFFUSION BARRIER NEEDED
900	2. USE PHOTO-DEFINABLE ILD
902	PASSIVATION DEP (NITRIDE)
904	POLYIMIDE PATTERN
906	DEVELOP POLYIMIDE
908	BLM DEP
910	PR COATING
912	PR (THICK METAL LAYER #1) PATTERN
914	CU PLATING
916	RESIST STRIP
918A	BLM ETCH/ASH
920	DEPOSIT DIELECTRIC (PHOTO-DEFINABLE POLYMER)
922	PHOTO-PATTERN VIAS
924	DEVELOP DIELECTRIC
926	BLM DEP
928	PR COATING
930	PR (THICK METAL LAYER #2) PATTERN
932	CU PLATING
934	RESIST STRIP
936	BLM ETCH/ ASH
938	DEPOSIT DIELECTRIC (PHOTO-DEFINABLE POLYMER)
940	PHOTO-PATTERN VIAS
942	DEVELOP DIELECTRIC
944	BLM DEP
946	PR COATING
948	BUMP PATTERN
950	BUMP PLATING
952	RESIST STRIP
	BLM ETCH/ASH

FIG. 9A

FLOW 2	
	1. NO CU DIFFUSION BARRIER NEEDED
900	2. USE PHOTO-DEFINABLE ILD
902	PASSIVATION DEP (NITRIDE)
904	POLYIMIDE PATTERN
906	DEVELOP POLYIMIDE
908	BLM DEP
910	PR COATING
912	PR (THICK METAL LAYER #1) PATTERN
914	CU PLATING
916	RESIST STRIP
918B	BLM ETCH/ASH
954	DEPOSIT DIELECTRIC (SELF-PLANARIZING POLYMER)
956	PR COATING
958	PATTERN VIAS
960	ETCH DIELECTRIC (DRY)
924	PR STRIP
926	BLM DEP
928	PR COATING
	PR (THICK METAL LAYER #2) PATTERN
930	CU PLATING
932	RESIST STRIP
934	BLM ETCH/ASH
962	DEPOSIT DIELECTRIC (SELF-PLANARIZING POLYMER)
964	PR COATING
966	PATTERN VIAS
968	ETCH DIELECTRIC (DRY)
970	PR STRIP
942	BLM DEP
944	PR COATING
946	BUMP PATTERN
948	BUMP PLATING
950	RESIST STRIP
952	BLM ETCH/ASH

FIG. 9B

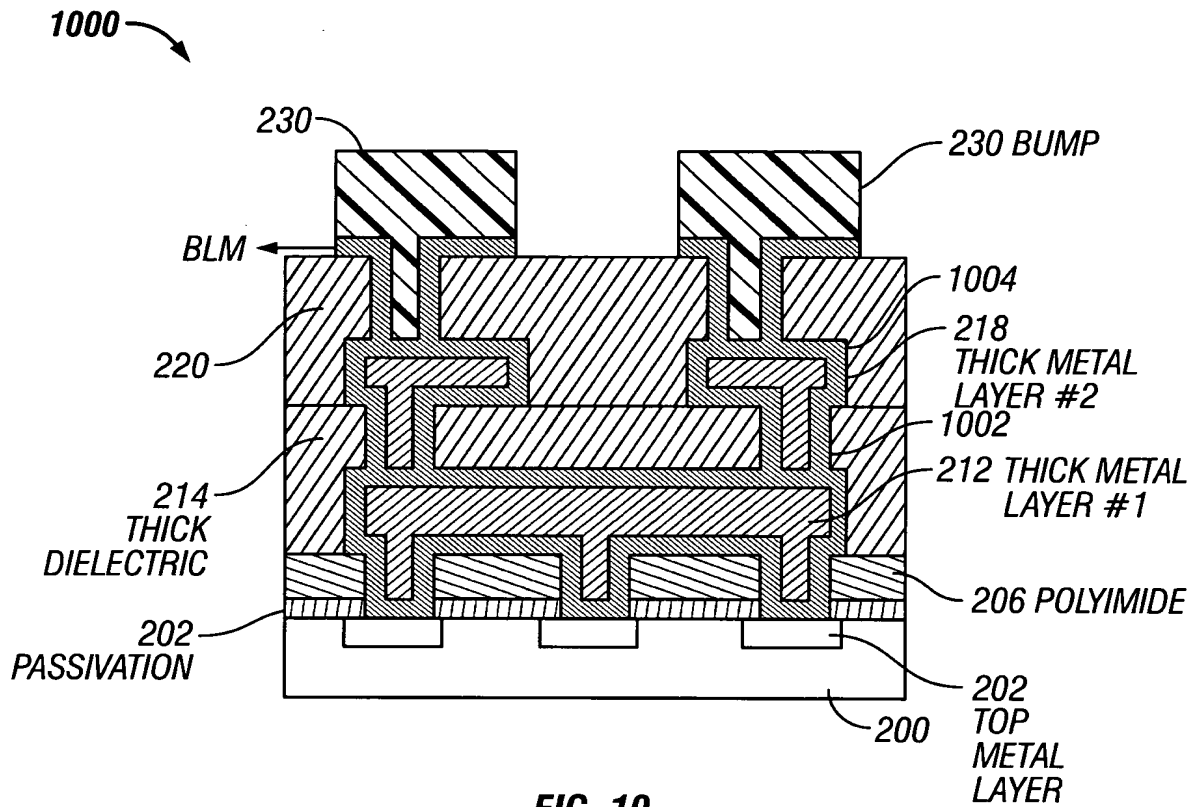


FIG. 10

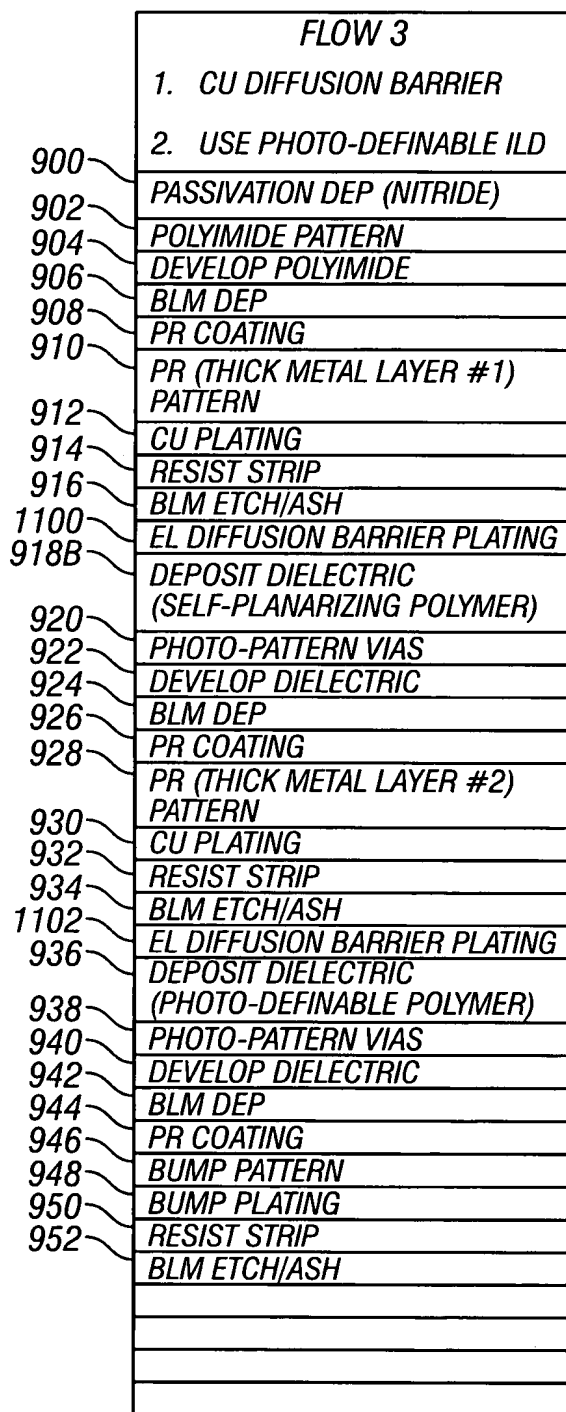


FIG. 11A

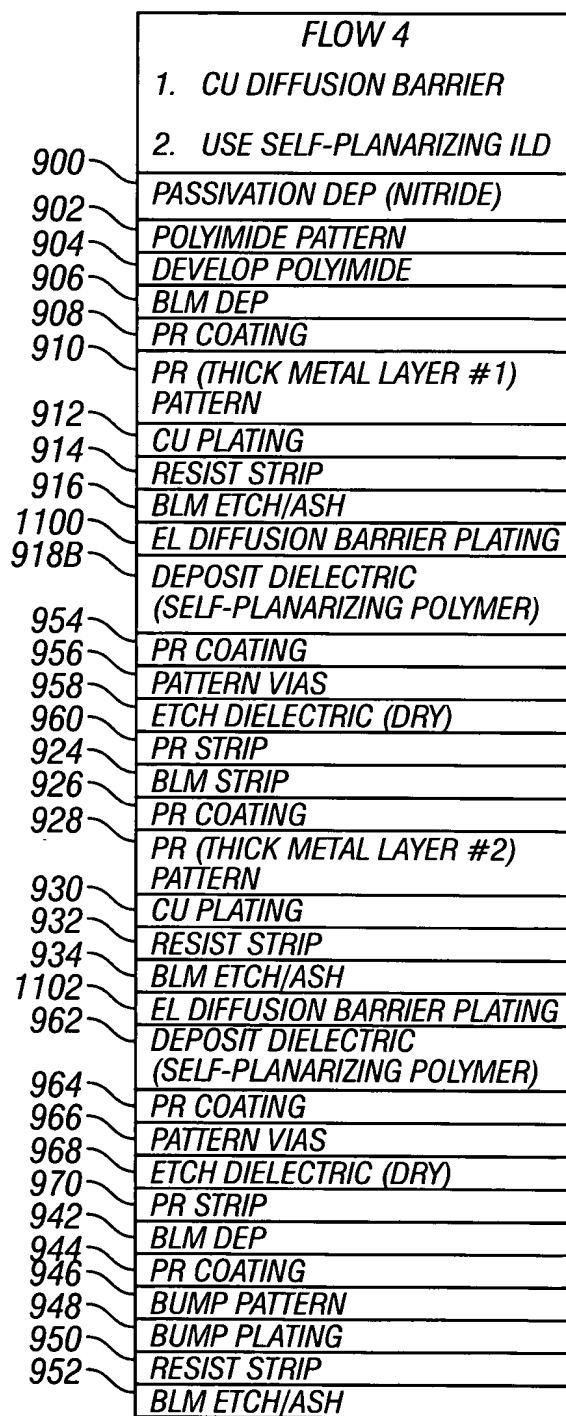


FIG. 11B

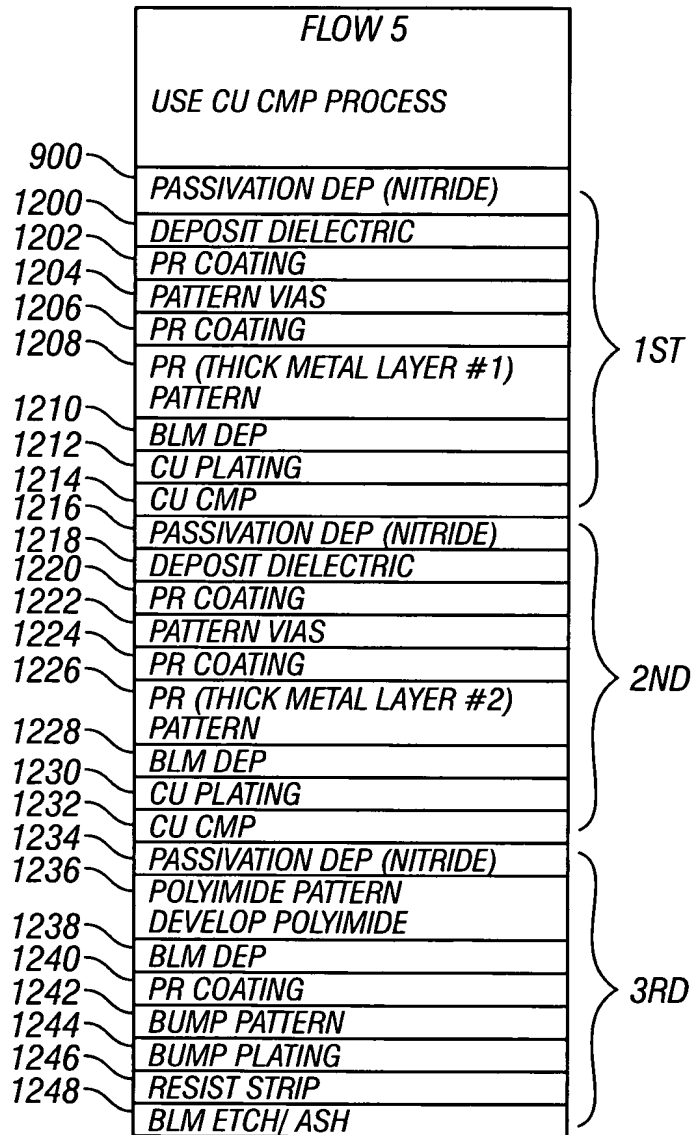


FIG. 12

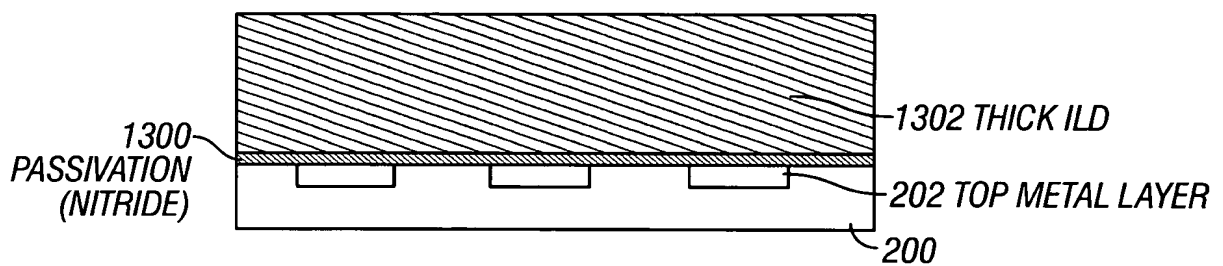


FIG. 13A

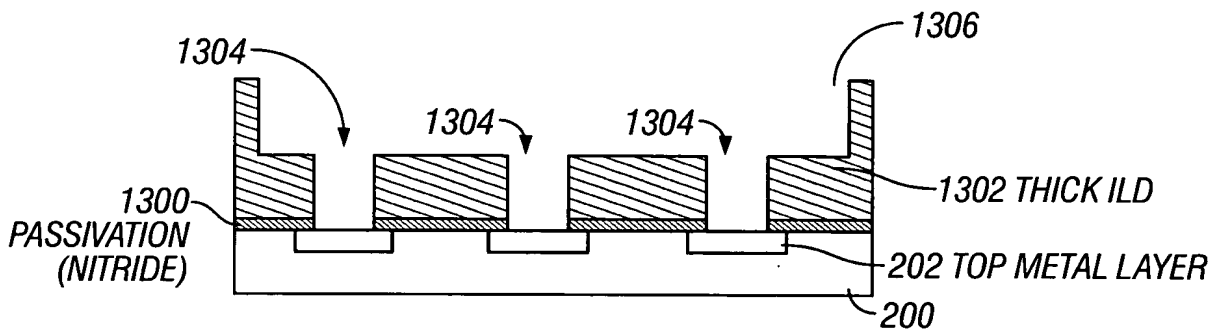
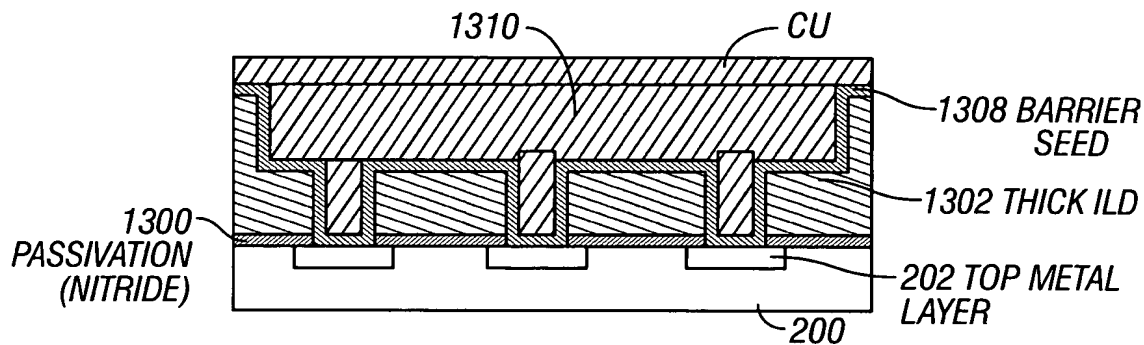
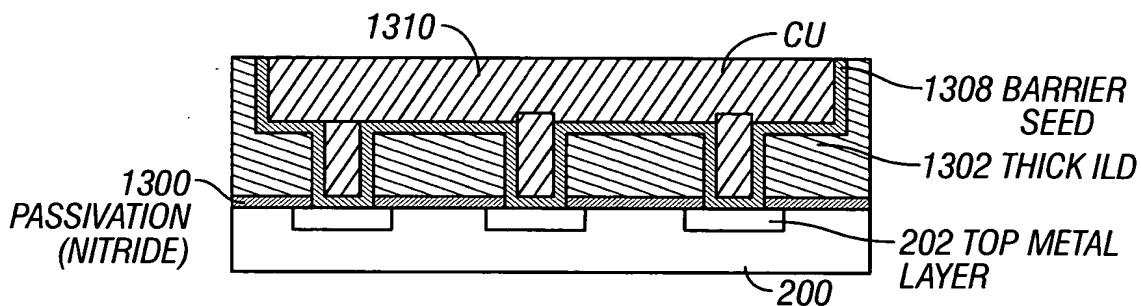


FIG. 13B

**FIG. 13C****FIG. 13D**

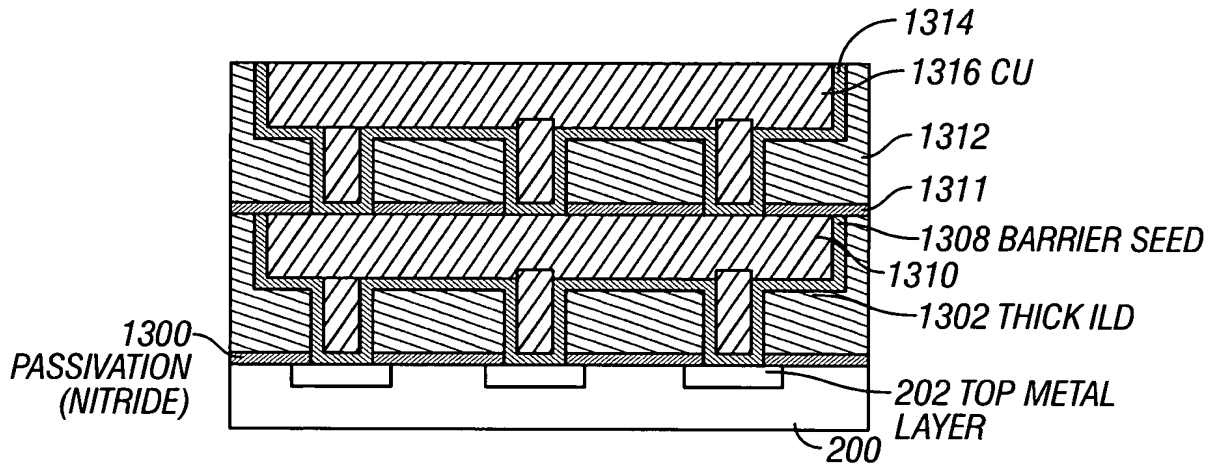


FIG. 13E

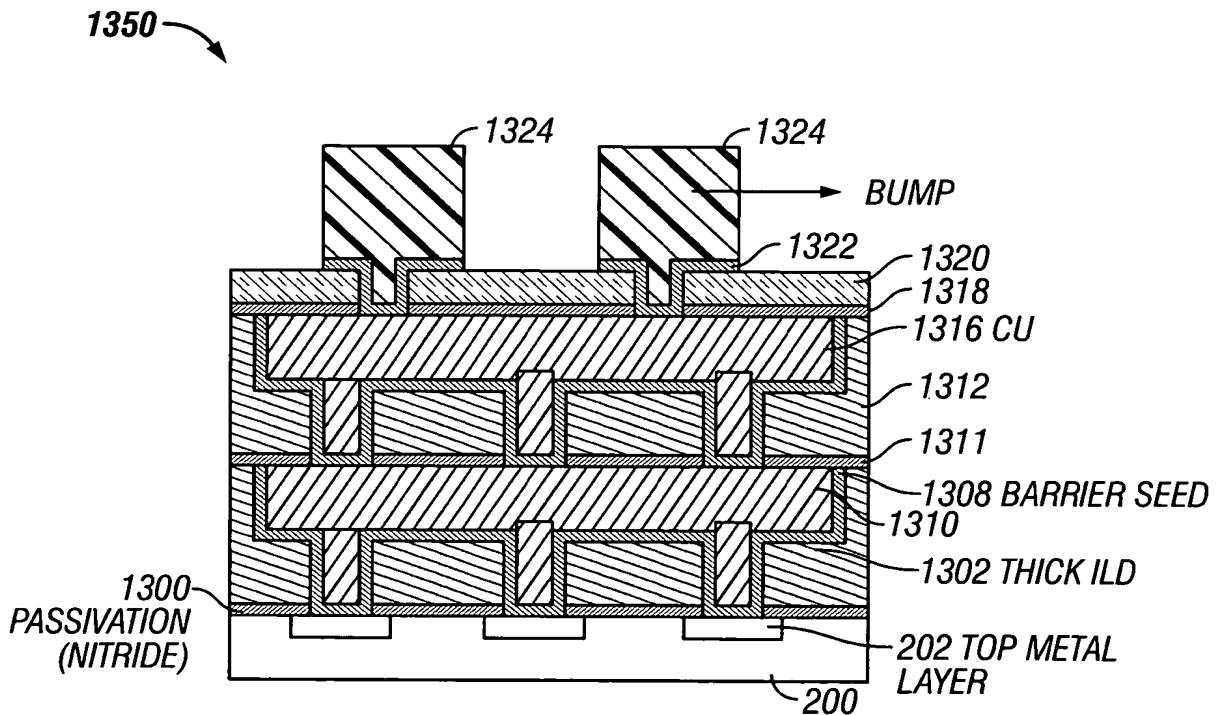


FIG. 13F

SIMULATION PARAMETERS			RESULTS	
ADDITION THICK METAL LAYERS	METAL WIDTH	VIA RESISTANCE (M Ω)	IMAX (MA)	IR DROP (MV)
1410 DEFAULT (PRESENT STATE OF ART)			680	29
1400 TWO 45 μ M THICK METAL LAYERS	70 μ M FOR METAL LAYER #2 100 μ M FOR METAL LAYER #1	0.7	430 (36% IMAX IMPROVEMENT)	30
1402 TWO 15 μ M THICK METAL LAYERS	70 μ M FOR METAL LAYER #2 100 μ M FOR METAL LAYER #1	0.7	530 (22% IMAX IMPROVEMENT)	30
1404 TWO 45 μ M THICK METAL LAYERS	70 μ M FOR METAL LAYER #2 100 μ M FOR METAL LAYER #1	70	370 (46% IMAX IMPROVEMENT)	49
1406 TWO 15 μ M THICK METAL LAYERS	70 μ M FOR METAL LAYER #2 100 μ M FOR METAL LAYER #1	70	380 (44% IMAX IMPROVEMENT)	51

FIG. 14

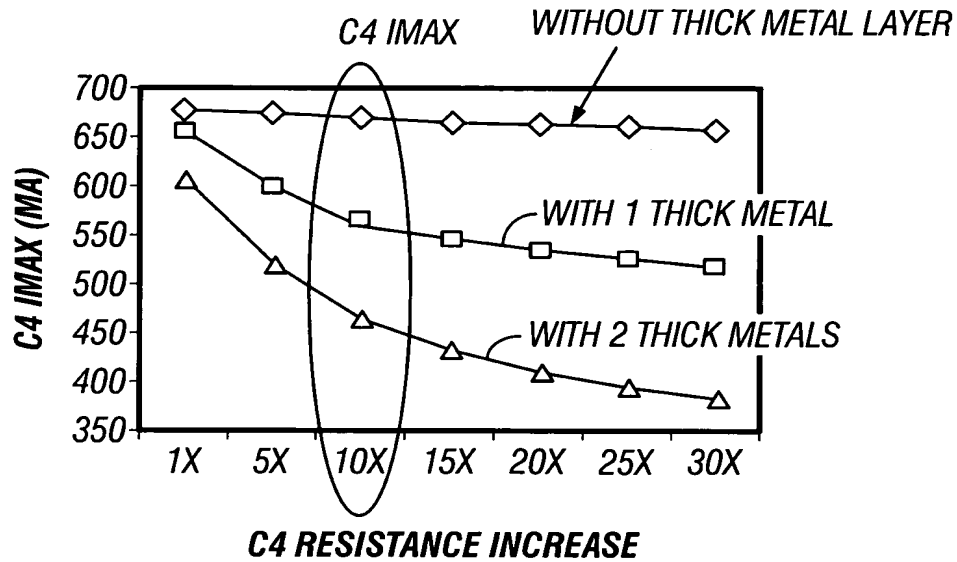


FIG. 15A

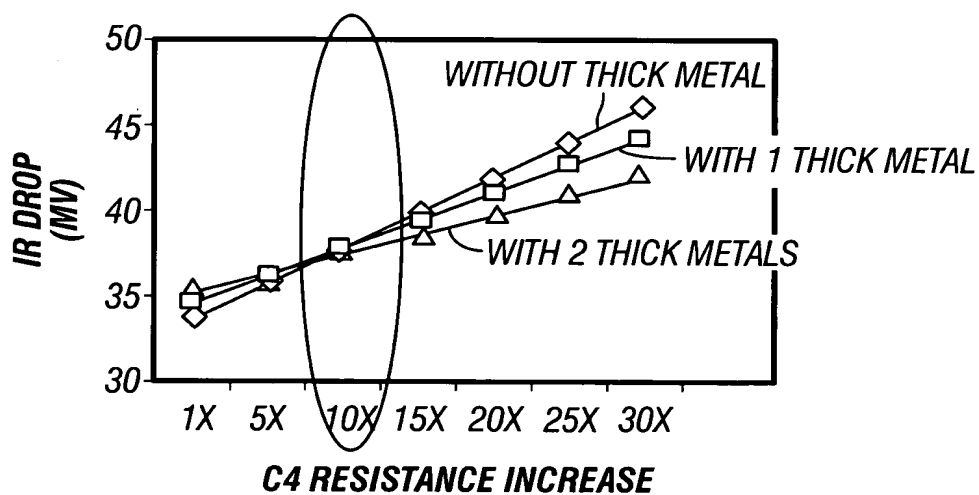


FIG. 15B

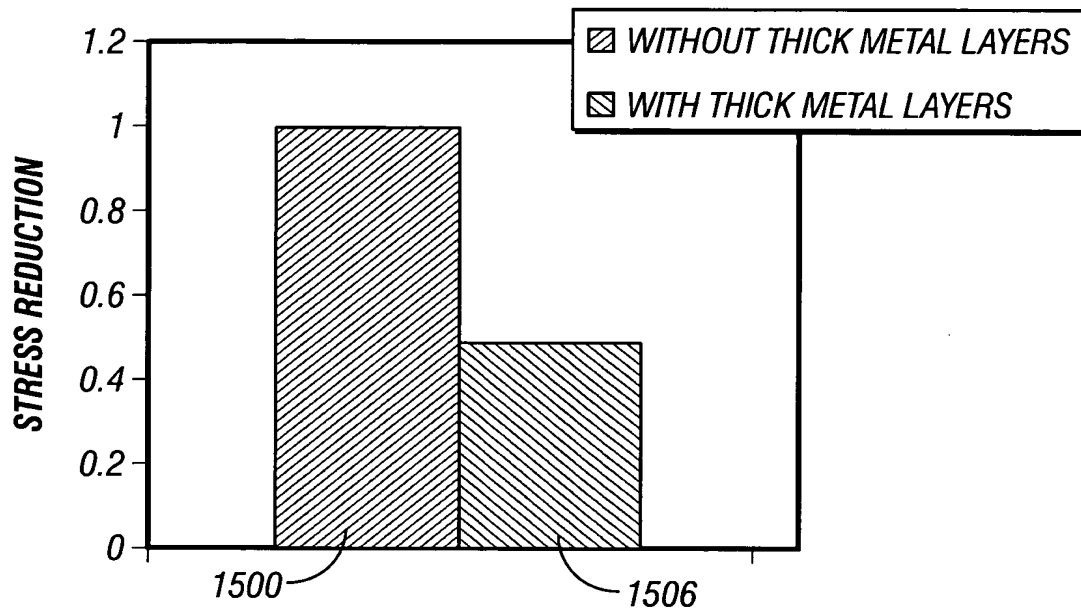


FIG. 16